

Power path: TYPE-C 5V <-FB-> +5P <-SW-> +5V <-SW-> +5V_OU_BATT <-Current Probe-> VDD <-LDO-> 3.3VP
+BATT in (3.0V to 4.2V)
VDD = system power

CORE: 1XX | COMM: 2XX | POWER: 3XX | EXT-INTERFACE: 5XX

